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**LIGHT EMITTING MODULE AND METHOD OF DRIVING THE SAME  
AND OPTICAL SENSOR**

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BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention relates to a module (hereinafter referred to as a light emitting module) including a device (hereinafter referred to as a light emitting device) comprising an element sandwiching a light emitting material (hereinafter referred to as a light emitting element) between electrodes. In particular, the present invention relates to a light emitting module comprising a light emitting element (hereinafter referred to as an EL element) using a compound producing EL (Electro Luminescence) as the light emitting material. Here, an organic EL display and an organic light emitting diode (hereinafter referred to as an OLED) is included in the light emitting device in accordance with the present invention.

Further, the light emitting material used in the present invention includes all materials emitting light (phosphorescence and/or fluorescence) through singlet excitation, triplet excitation or both of them.

2. Description of the Related Art

In recent years, the development of an EL element using an organic compound (hereinafter referred to as an organic EL film ) producing EL (Electro Luminescence) as a light emitting layer has advanced and EL elements using various kinds of organic EL films have been proposed. A flat panel display using such an EL element as a light emitting element has been developed.

A passive matrix type light emitting device and an active matrix type light emitting device have been known as a light emitting device employing an EL element.

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### SUMMARY OF THE INVENTION

A light emitting module in accordance with the present invention is characterized in that it includes a sensor section to sense the illuminance of an environment where the light emitting module is used (hereinafter referred to as environmental illuminance) and means for adjusting the luminance of a light emitting element according to the environmental illuminance and for keeping the ratio of the luminance of the light emitting element to the environmental illuminance (contrast ratio between the luminance of the light emitting element and the environmental illuminance) at a constant value.

In other words, the light emitting module in accordance with the present invention is characterized in that the luminance of an EL element can be increased in a bright use environment to improve visibility and that the luminance of the EL element can be decreased in a dark use environment to reduce power consumption without degrading visibility.

It is recommended that the environmental illuminance be sensed (monitored) by an optical sensor. The present invention is characterized also in that a sensor section including one or a plurality of optical sensors (typically, photodiodes) and a pixel section for displaying an image are formed on the same insulating body. That is, the present invention is characterized also in that the sensor section including photodiodes is formed with the same process as a transistor (including a thin film transistor and a MOS transistor using bulk silicon) and the EL element on the pixel section.

In the light emitting module in accordance with the present invention, the environmental illuminance is sensed by the sensor section formed in the light emitting

device, and the correct luminance of the EL element and a correction signal which is necessary for obtaining the correct luminance of the EL element are calculated by a correction circuit based on the output signal of the sensor section. Then, the amount of current flowing through the EL element is corrected based on the correction signal to keep the ratio of the luminance of the EL element to the environmental illuminance (contrast ratio) at a constant value.

The light emitting module in accordance with the present invention is excellent in visibility in a bright environment because of a sufficiently bright display and reduces power consumption in a dark environment because it is possible to decrease brightness to a minimum while ensuring good visibility. Therefore, an electric appliance employing the light emitting module in accordance with the present invention has excellent visibility in a display section and can reduce power consumption.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Preferred embodiments of the present invention will be described in detail based on the following figures, in which:

FIGS. 1A to 1C show the configuration of a light emitting module;

FIG. 2 shows the configuration of a correction circuit;

FIG. 3 shows the configuration of a correction circuit;

FIG. 4 shows the cross-sectional structure of a light emitting module;

FIGS. 5A to 5E show the manufacturing process of a light emitting module;

FIG. 6 shows the cross-sectional structure of a light emitting module;

FIG. 7 shows the cross-sectional structure of a light emitting module;

FIG. 8 shows the configuration of a light emitting module;

FIGS. 9A and 9B show the configuration of an optical sensor;

FIG. 10 shows the configuration of an optical sensor;

FIGS. 11A and 11B show the configuration of a pixel section of a light emitting module;

FIGS. 12A and 12B show the top structure and cross-sectional structure of a light emitting module;

FIGS. 13A and 13B show the structure of a light emitting module of the type in which a driving circuit is built;

FIGS. 14A and 14B show the structure of a light emitting module having an external controller;

FIGS. 15A to 15F show a specific example of an electrical appliance;

FIG. 16A and 16B show a specific example of an electrical appliance; and

FIG. 17 shows the cross-sectional structure of a light emitting module.

### DESCRIPTION OF THE PREFERRED EMBODIMENTS

The preferred embodiments in accordance with the present invention will be described. FIG. 1A is a circuit block diagram of a light emitting module in accordance with the present invention. A light emitting device 100 includes a pixel section 101, a data signal (video signal) side driving circuit 102, gate signal side driving circuit 103, and a sensor section 104, and a correction circuit 105 is connected to the light emitting device 100. The correction circuit 105 has an arithmetic circuit for computing the luminance of the light emitting element of the pixel section 101 based on a signal transmitted by the sensor section 104.

A monolithic IC, a hybrid IC, or a MCM (Multi Chip Module) may be used as the correction circuit 105. When the monolithic IC is used, it may be directly

packaged into the light emitting device 100 and may be packaged on a TAB (Tape Automated Bonding) tape and be connected to the light emitting device 100 as a TCP (Tape Carrier Package). Also, when the hybrid IC or the MCM is used, it is recommended to be connected to the light emitting device 100 with the use of the TAB tape.

Next, FIG. 1B shows one example of a circuit configuration of the sensor section 104. Here, the sensor section 104 includes a photodiode 106, a reset TFT 107, a buffer TFT 108, and a constant current TFT 109.

The reset TFT 107 is a TFT for applying a reverse bias voltage to the photodiode 106 to return (reset) to the initial state, and a timing of returning to the initial state is controlled by a signal transmitted to a reset signal line 110 which is a gate. Further, the buffer TFT 108 is a TFT for amplifying a signal sensed by the photodiode 106 and the constant current TFT 109 is a TFT which functions as a constant current power source. Here, the buffer TFT 108 and the constant current TFT 109 function as a source follower and an output signal is transmitted to an output line 111.

Here, constant voltages V1 to V3 are fixed voltages applied to the photodiode 106, the reset TFT 107, the buffer TFT 108, and the constant current TFT 109. Typically, a power source voltage or an earth voltage is employed as the fixed voltage.

Here, the circuit configuration shown in FIG. 1B is one example and any publicly known circuit configuration may be employed providing that the circuit configuration functions as an optical sensor. Further, while the TFT is used as an active device in this example, in the case where the pixel section is formed of a MOS transistor (a transistor with a MOS structure formed on a semiconductor substrate), naturally, the MOS transistor is used.



correct value data of luminance corresponding to the environmental illuminance so as to ensure a constant contrast ratio of the luminance to the environmental illuminance. Of course, it is necessary to previously get and store the correct value data of the luminance corresponding to the environmental illuminance.

Here, the flow of a signal in the case of the analog driving system shown in FIG. 2 will be described. In the case of the analog driving system, a signal which determines the amount of current to the EL element is a signal transmitted to the data line 117 in FIG. 1C.

Data of environmental illuminance transmitted from the sensor section 104 (sensor output signal) is converted into a digital signal by the A/D conversion circuit 201 and is inputted to the arithmetic circuit 202. The arithmetic circuit 202 calculates a correct value of a data signal (video signal) for obtaining correct luminance with respect to the environmental illuminance based on the inputted sensor output signal and the data stored in the correction memory 203.

In this manner, the data signal (video signal) from a signal generator 205 is corrected to the correct value based on the sensor output signal and the data stored in the correction memory 203, and the corrected data signal is again converted into an analog signal by the D/A conversion circuit 204 and is inputted to the data signal side driving circuit 102.

Next, the flow of a signal in the case of the digital driving system shown in FIG. 3 will be described. In the case of the digital driving system, a signal which determines the amount of current to the EL element is a signal transmitted to the current supply line 118 in FIG. 1C.

Data of environmental illuminance transmitted from the sensor section 104 (sensor output signal) is converted into a digital signal by the A/D conversion circuit



301 and is inputted to the arithmetic circuit 302. The arithmetic circuit 302 calculates a correct value of the amount of current which is necessary for obtaining correct luminance with respect to the environmental illuminance based on the inputted sensor output signal and the data stored in the correction memory 303, and a correction signal with its information is outputted.

In this manner, the correction signal calculated based on the sensor output signal and the data stored in the correction memory 303 is converted into an analog signal by the D/A conversion circuit 304 and is inputted to an EL driving power source 305. The EL driving power source 305 is a power source of a signal (hereinafter referred to as a power source data signal) transmitted to the current supply line of the pixel section 101, and a power source for finally determining a current flowing to the EL element. A voltage varying device 306 is connected to the EL driving power source 305 and a power source data signal is corrected based on the correction signal transmitted from the correction circuit 105 and the corrected power source data signal is inputted to the pixel section 101.

In this manner, first, the environmental illuminance is sensed by the sensor section 104 provided in the light emitting device and the correction circuit 105 calculates a data signal or a correction signal which is necessary for obtaining the correct luminance of the EL element based on the output signal (sensor output signal). Then, the amount of current flowing to the EL element is corrected based on this data signal or correction signal to produce luminance of a correct contrast ratio.

The light emitting module described in the preferred embodiment in accordance with the present invention is excellent in visibility in a bright environment because of a sufficiently bright display and reduces power consumption in a dark environment because it is possible to reduce brightness to a minimum while ensuring

good visibility. Accordingly, in an electric appliance using a light emitting module of the present invention, a display is excellent in visibility and power consumption is reduced.

(Embodiments)

(Embodiment 1)

In the present embodiment, a cross-sectional structure (in a state before sealing) included in a light emitting module in accordance with the present invention will be described. In the present embodiment, there will be described an example of a light emitting device (in a state before sealing) which has a sensor section, a pixel section, and a driving circuit for driving the pixel section on the same insulating body. Here, the sensor section shows a reset TFT and a photodiode connected to the reset TFT, and the driving circuit shows a CMOS circuit which is a basic unit, and the pixel section shows one pixel.

In FIG. 4, a reference numeral 400 is an insulting body (made of an insulating substrate, an insulating film, or a substrate with an insulating film on the surface) on which a sensor section, a driving circuit, and a pixel section are formed. The sensor section is provided with a reset TFT 451 and a photodiode 452. Also, the driving circuit has an n-channel type TFT 453 and a p-channel type TFT 454 which constitute a CMOS circuit. Further, the pixel section has a switching TFT 455, a current control TFT 456, and an EL element 457. In this respect, the respective TFTs may be TFTs with any publicly known structure. In the present embodiment, the respective TFTs are bottom gate type TFTs (specifically, inverse stagger type TFT), but top gate type TFTs (typically, planar type TFT) may be used.

Further, the circuit configuration of the sensor section in the present embodiment has a structure shown in FIG. 1B and the circuit configuration of the pixel

section has a structure shown in FIG. 1C. However, not only such a circuit configuration but also a circuit with three or more TFTs can produce the effects of the present invention.

Here, the structures of the respective TFTs formed on the insulating body 400 will be described. In the n-channel type TFT 453, a reference numeral 401 designates a gate electrode, 402 designates a gate insulating film, 403 designates a source region made of an n-type semiconductor region (hereinafter referred to as n-type region), 404 designates a drain region made of an n-type region, 405a and 405b designate an LDD (lightly doped drain) region, 406 designates a channel forming region, 407 designates a channel protecting film, 408 designates a first interlayer insulating film, 409 designates a source wiring, and 410 designates a drain wiring.

Further, in the p-channel type TFT 454, a reference numeral 411 designates a gate electrode, 402 designates a gate insulating film, 412 designates a source region made of a p-type semiconductor region (hereinafter referred to as p-type region), 413 designates a drain region made of a p-type region, 414 designates a channel forming region, 415 designates a channel protecting film, 408 designates a first interlayer insulating film, 416 designates a source wiring, and 410 designates a drain wiring which is a wiring common to the n-channel type TFT 453.

Further, basically, the reset TFT 451 has the same structure as the n-channel type TFT 453 (they are different from each other only in the structure of the source wiring or the drain wiring) and hence the detailed description thereof will be omitted. Here, it is also possible that the reset TFT 451 has the same structure as the p-channel type TFT 454. In the case of the reset TFT 451, an amorphous semiconductor film (typically, an amorphous silicon film) 419 is formed between a drain region 417 made of an n-type region and a p-type region 418 to form a photodiode 452 with a PIN

junction. Here, a reference numeral 420 designates a wiring for applying voltage to the p-type region 418.

Further, basically, the switching TFT 455 has the same structure as the n-channel type TFT 453 and hence the detailed description thereof will be omitted. Here, it is also possible to form the switching TFT 455 with the same structure as the p-channel type TFT 454. Further, it is also possible to form the switching TFT 455 with a structure in which two or more channel forming regions are formed between the source region and the drain region (multi-gate structure).

Further, basically, the current control TFT 456 has the same structure as the p-channel type TFT 454 (they are different from each other in that the drain wiring is a pixel electrode 423) and hence the detailed description thereof will be omitted. Here, it is also possible to form the current control TFT 456 having the same structure as the n-channel type TFT 453.

Then, there is formed a second interlayer insulating film (leveling film) 421 covering the reset TFT 451, the photodiode 452, the n-channel type TFT 453, the p-channel type TFT 454, the switching TFT 455, and the current control TFT 456.

Still further, the second interlayer insulating film 421 has a contact hole extending to the drain region 422 of the current control TFT 456 and a pixel electrode 423 is connected to the drain region 422. The pixel electrode 423 functions as an anode of the EL element and is formed of a conductive film with a large work function, typically, an oxide conductive film. It is recommended that the oxide conductive film be made of indium oxide, tin oxide, zinc oxide, or a compound thereof. Also, gallium oxide may be added to the oxide conductive film.

Next, a reference numeral 424 designates an insulating film covering the end portion of the pixel electrode 423 and is called a bank in the present specification. It

is recommended that the bank 424 be formed of an insulating film containing silicon or a resin film. In the case of using the resin film, a dielectric breakdown can be prevented in the film formation if carbon particles or metal particles are added to the resin film so that the specific resistance of the resin film becomes from  $1 \times 10^6 \Omega\text{m}$  to  $1 \times 10^{12} \Omega\text{m}$  (preferably, from  $1 \times 10^8 \Omega\text{m}$  to  $1 \times 10^{10} \Omega\text{m}$ ).

Next, a reference numeral 425 designates an EL layer. In this respect, in the present specification, a laminated body of a combination of layers selected from a hole injection layer, a hole transport layer, a hole prevention layer, an electron transport layer, an electron injection layer and an electron prevention layer is defined as an EL layer with respect to a light emitting layer. The light emitting layer may be formed of any publicly known material and a publicly known dopant (typically, fluorescent dye) may added to the light emitting layer. Further, it is preferable that an organic material which emits light through triplet excitation is used as the dopant since high luminous efficiency can be produced.

Next, a reference numeral 426 designates a cathode of the EL element and is formed of a conductive film with a small work function. It is recommended that a conductive film containing an element which belongs to the first or second group of a periodic table be used as the conductive film with a small work function. In the present embodiment, a conductive film made of a compound of lithium and aluminum is used.

In this respect, a laminated body 457 of the pixel electrode (anode) 423, the EL layer 425, and the cathode 426 is an EL element. Light generated by the EL element 457 is emitted to the insulating body 400 (in the direction shown by an arrow in Fig. 4). Also, in the case of using the p-channel type TFT as the current control TFT, as described in the present embodiment, it is preferable that the anode of the EL

element is connected to the drain of the current control TFT.

In this respect, it is effective that a protective film (passivation film) 427 completely covering the EL element 457 is formed after the cathode 426 is formed. A single layer of an insulating film of a carbon film, a silicon nitride film, or a silicon nitride oxide film or a laminated layer of a combination of the above insulating films is used as the protective film 427.

Here, it is preferable that a film with good coverage is used as the protective film 427, and a carbon film, a DLC (diamond like carbon) film in particular, is effectively used. The DLC film can be formed in a temperature range of the room temperature to 100°C and hence can be easily formed also on the EL layer 425 with low heat resistance. Further, the DLC film has a high blocking effect to oxygen and it is possible to effectively prevent oxidation of the EL layer 425. Accordingly, the DLC film can prevent the EL layer 425 from being oxidized during the following sealing process.

Here, a manufacturing process for producing the structure shown in FIG. 4 will be shown in FIG. 5. First, gate electrodes 502 to 506 are formed of chromium film on a glass substrate 501 and a gate insulating film 507 is formed thereon of a silicon nitride oxide film (insulating film expressed by  $\text{SiO}_x\text{N}_y$ ). An amorphous silicon film is formed on the gate insulating film 507 and crystallized by laser annealing to form semiconductor films 508 to 513 of a crystalline silicon film by patterning. The process up to this point may be performed by using the publicly known materials and technology (see FIG. 5A).

Here, the distance between the semiconductor film 508 and the semiconductor film 509 is not more than 1  $\mu\text{m}$ , preferably, from 0.3  $\mu\text{m}$  to 0.5  $\mu\text{m}$ .

Next, insulating films 514 to 519 made of a silicon oxide film are formed on

the semiconductor films 508 to 513 and phosphorus or arsenic is added thereto by the publicly known method. In this way, n-type regions 520 to 525 are formed. The n-type regions 520 to 525 contain phosphorus or arsenic with a concentration of  $1 \times 10^{20}$  atoms/cm<sup>3</sup> to  $1 \times 10^{21}$  atoms/cm<sup>3</sup> (see FIG. 5B).

Next, the insulating films 514 to 519 are patterned by back-surface lithography with using the gate electrodes 502 to 506 as masks to form insulating films (channel protecting film) 526 to 530. Then, in this state, phosphorus or arsenic is again added thereto by the publicly known method. In this way, n-type regions 531 to 541 are formed. The n-type regions 531 to 541 contain phosphorus or arsenic with a concentration of  $1 \times 10^{17}$  atoms/cm<sup>3</sup> to  $1 \times 10^{19}$  atoms/cm<sup>3</sup> (see FIG. 5C).

Next, resist films 542 to 544 are formed and boron is added thereto by the publicly known method. In this way, p-type regions 545 to 549 are formed. The p-type regions 545 to 549 contain boron with a concentration of  $3 \times 10^{20}$  atoms/cm<sup>3</sup> to  $5 \times 10^{21}$  atoms/cm<sup>3</sup>. Here, although phosphorus or arsenic is already added to the p-type regions 545 to 549, the concentration of boron is three or more times that of phosphorus or arsenic and hence the p-type regions 545 to 549 are completely inverted from n type to p type. (see FIG. 5D).

Next, the resist films 542 to 544 are removed and a first interlayer insulating film 550 with a laminated structure of a silicon oxide film and a silicon nitride oxide film is formed. Contact holes are made in the first interlayer insulating film 550 and wirings 551 to 558 with a laminated structure of molybdenum and tungsten are formed. Then, a conversion layer 559 made of a semiconductor film is formed. The conversion layer 559 is a layer for absorbing light and generating carriers in the photodiode and corresponds to the i- layer (photoelectric conversion layer) of a solar cell. (see FIG. 5E).

In this respect, a publicly known layer structure with a PIN junction can be used as the conversion layer 559. Also, the conversion layer 559 may have a PIN junction or a NIP junction in viewing from the incident side of light. Further, an amorphous semiconductor film, a crystalline semiconductor film, or a microcrystalline semiconductor film may be used as the material of the conversion layer 559.

Then, as shown in FIG. 4, a second interlayer insulating film 421, a pixel electrode 423, a bank 424, an EL layer 425, a cathode 426, and a protective film 427 are formed to complete a light emitting device with a cross-sectional structure shown in FIG. 4.

If a light emitting device with the cross-sectional structure of the present embodiment is employed, it is possible to provide a light emitting module which is excellent in visibility in the bright environment and can ensure good visibility while reducing power consumption in the dark environment. Here, in the present embodiment, the configurations shown in FIG. 1 and FIG. 2 or FIG. 1 and FIG. 3 may be combined.

#### (Embodiment 2)

In the present embodiment, there will be described an example of a light emitting device (however, in the state before sealing) with a structure different from the embodiment 1. Here, in the present embodiment, description will be made in the different parts from the embodiment 1. The description of the embodiment 1 may be referred to on parts with the same reference symbols as the parts in FIG. 4.

In FIG. 6, a sensor section, a driving circuit, and a pixel section are formed on an insulating body 400. The sensor section includes a reset TFT 451 and a photodiode (photo-sensor) 601, and the driving circuit includes a CMOS circuit comprising an n-channel type TFT 453 and a p-channel type TFT 454, and the pixel



section includes a switching TFT 455, a current control TFT 456, and an EL element 457.

The present embodiment is different from the embodiment 1 in the structure of the photodiode 601. The photodiode is formed of a wiring 603 which is to be the source or drain of the reset TFT 451, a conversion layer 604, and a reflection side electrode (electrode at the reflecting side of light) 605. Also, in the present embodiment, a buffer layer 606 made of an insulating film containing silicon is formed on a second interlayer insulating film 421. The buffer layer 606 makes the wiring 603 put into close contact with the second interlayer insulating film 421 and allows second interlayer insulating film 421 to be prevented from being etched when the conversion layer 604 is formed.

Here, the wiring 603 is transparent with respect to visible light because it is formed by the same process as the pixel electrode 423. Also, a conductive film to be the reflection side electrode is preferably a conductive film with a high reflectivity, and it is recommended that a conductive film containing aluminum or silver as a main component be used. Here, if an oxide conductive film is formed as a buffer layer between the conversion layer 604 and the reflection side electrode 605, the conversion layer 604 can be prevented from reacting with the reflection side electrode 605.

If a light emitting device with the cross-sectional structure of the present embodiment is employed, it is possible to provide a light emitting module which is excellent in visibility in the bright environment and can ensure good visibility while reducing power consumption in the dark environment. Here, in the present embodiment, the configurations shown in FIG. 1 and FIG. 2 or FIG. 1 and FIG. 3 may be combined.

(Embodiment 3)

In the present embodiment, there will be described an example of a light emitting device (however, in the state before sealing) with a different structure from the embodiment 1. Here, in the present embodiment, description will be made in the different parts from the embodiment 1. The description of the embodiment 1 may be referred to on parts with the same reference symbols as the parts in FIG. 4.

In FIG. 7, a sensor section, a driving circuit, and a pixel section are formed on an insulating body 400. The sensor section includes a reset TFT 701 and a photodiode (photo-sensor) 702, and the driving circuit includes a CMOS circuit comprising an n-channel type TFT 703 and a p-channel type TFT 704, and the pixel section includes a switching TFT 705, a current control TFT 706, and an EL element 457.

First, the present embodiment is characterized in that the source line or the drain line of each TFTs is formed so as to cover a channel region. The structure of the present embodiment is formed in the shape shown in FIG. 7 in order to obstruct light which directly enters to the channel forming region of the TFT to prevent an increase in leakage current.

Further, in the present embodiment, an n-channel type TFT is used as a current control TFT 706. Here, the current control TFT 706 has basically the same structure as the n-channel type TFT 453 shown in FIG. 4 (they are different from each other only in the shape of a source electrode), so the detailed description thereof will be omitted. Here, the current control TFT 706 can have the same structure as the p-channel type TFT 454 shown in FIG. 4.

Still further, the present embodiment is different from the embodiment 1 in the structure of a photodiode 702 and the photodiode 702 is formed of a wiring 711 to be the source or drain of the reset TFT 701, a conversion layer 712, and a transmission

side electrode (electrode at the side where light transmits) 713, and a leader line 714 is connected to the transmission side electrode 713. The conversion layer 712 can be formed in the same configuration as the conversion layer 604 of the embodiment 2. Also, it is recommended that the transmission side electrode 713 be formed of an oxide conductive film.

In a pixel section, a pixel electrode 715 is formed in the same process as the leader line 714. The pixel electrode 715 is an electrode which functions as the cathode of an EL element 707 and is formed of a conductive film containing an element which belongs to the first group or the second group of the periodic table. In the present embodiment, a conductive film made of a compound of lithium and aluminum is used. Here, in the case where a cathode is connected to the current control TFT as described in the present embodiment, it is preferable that an n-channel type TFT is used as the current control TFT.

After the pixel electrode 715 is formed, an insulating film (bank) 424, an EL layer 716, an anode 717 made of an oxide conductive film, and a protective film 718 are formed to complete a light emitting device (however, in the state before sealing) with the structure shown in FIG. 7. It is recommended that the material and the structure of the EL layer 716, the anode 717, and the protective film 718 be referred to the embodiment 1.

If a light emitting device with the cross-sectional structure of the present embodiment is employed, it is possible to provide a light emitting module which is excellent in visibility in the bright environment and can ensure good visibility while reducing power consumption in the dark environment. Here, in the present embodiment, the configurations shown in FIG. 1 and FIG. 2 or FIG. 1 and FIG. 3 may be combined.

(Embodiment 4)

In the present embodiment, there will be described an example of a light emitting device (however, in the state before sealing) with a different structure from the embodiment 1. Here, in the present embodiment, description will be made in the different parts from the embodiment 1. The description of the embodiment 1 may be referred to on parts with the same reference symbols as the parts in FIG. 4.

In FIG. 17, a sensor section and a pixel section are formed on an insulating body 400. Here, a driving circuit can be formed on the same insulating body as the embodiment 2 or the embodiment 3.

The sensor section includes a reset TFT 1701 and a photodiode (photo-sensor) 1702 and the pixel section includes a switching TFT 1703, a current control TFT 1704, and an EL element 1705.

In the present embodiment, a p-channel type TFT is used as the current control TFT 1704. Here, the current control TFT 1704 basically has the same structure as the current control TFT 456 shown in FIG. 4 and, hence, the detailed description will be omitted. Here, the current control TFT 1704 can also have the same structure as the n-channel type TFT 455 shown in FIG. 4.

Further, the present embodiment is different from the embodiment 1 in the structure of the photodiode 1702, and the photodiode 1702 is formed of a wiring 1711 made of an oxide conductive film to be the source or the drain of the reset TFT 1701, an n-type semiconductor layer 1712, a conversion layer (i-type semiconductor layer) 1713, a p-type semiconductor layer 1714, and a light receiving side electrode (electrode at the side where light is received) 1715.

Here, the wiring 1711 is formed in the same process as the pixel electrode (anode of the EL element 1705) 1716. Also, the light receiving side electrode 1715

may be formed of an oxide conductive film. Here, in the pixel section, the pixel electrode 1716 formed in the same process as the wiring 1711 is electrically connected to the drain of the current control TFT 1704.

After the pixel electrode 1716 is formed, an insulating film (bank) 424, an EL layer 425, a cathode 426, and a protective film 427 are formed to complete a light emitting device (however, in the state before sealing) with the structure shown in FIG. 17. It is recommended that the material and the structure of the EL layer 425, the cathode 426, and the protective film 427 be referred to the embodiment 1.

If a light emitting device with the cross-sectional structure of the present embodiment is employed, it is possible to provide a light emitting module which is excellent in visibility in the bright environment and can ensure good visibility while reducing power consumption in the dark environment. Here, in the present embodiment, the configurations shown in FIG. 1 and FIG. 2 or FIG. 1 and FIG. 3 may be combined.

(Embodiment 5)

In the present embodiment, a light emitting device, in which a sensor section, a correction circuit, a driving circuit, and a pixel section are formed on the same insulating body, will be described.

In FIG. 8, a reference numeral 800 designates a light emitting device of the present embodiment, 801 designates a pixel section, 802 designates a data signal side driving circuit, 803 designates a gate signal side driving circuit, 804 designates a sensor section, and 805 designates a correction circuit. Here, it is recommended that the configuration of the pixel section 801 be referred to FIG. 1C and that the configuration of the sensor section 804 be referred to FIG. 1B.

The present embodiment is characterized in that the correction circuit 805

with the configuration shown in FIG. 2 or FIG. 3 is formed on the same insulating body as the sensor section, the driving circuit, the pixel section. That is, in the light emitting device 800 of the present embodiment, a corrected data signal is outputted from a correction circuit 805 based on the environmental illuminance sensed by the sensor section 804 to adjust the luminance of the pixel section 801 to a correct intensity.

Of course, in the configuration of the present embodiment, the correction circuit 805 is formed only of transistors (TFT or MOS transistor). Here, it is recommended that the correction circuit be designed by using a CMOS circuit comprising the n-channel type TFT 453 and p-channel type TFT 454 in FIG. 4 as a basic section.

In this respect, the configuration of the present embodiment can be carried out with any of the configurations of the embodiment 1 to the embodiment 4. Since the light emitting module including the light emitting device of the present embodiment has a built-in correction circuit, the weight can be reduced as compared with the structure shown in FIG. 1 and the number of pins which is necessary for connecting the correction circuit to the driving circuit can be reduced.

(Embodiment 6)

A circuit which functions as an optical sensor (photo sensor) can be used as a sensor section included in a light emitting module of the present invention. In the present embodiment, examples of the circuit configuration of an active type optical sensor will be shown in FIGS. 9A and 9B.

The optical sensor shown in FIG. 9A includes a photodiode 901, a first reset TFT 902, a buffer TFT 903, a load capacitance 904, a second reset TFT 905. Also, a first reset signal line 906 is connected to the gate of the first reset TFT 902 and a

second reset signal line 907 is connected to the gate of the second reset TFT 905. Further, a reference numeral 908 designates an output line.

Further, the optical sensor shown in FIG. 9B includes a photodiode 911, a reset TFT 912, a buffer TFT 913, a load resistance (or a load capacitance) 914. Also, a reset signal line 915 is connected to the gate of the reset TFT 912. Further, a reference numeral 916 designates an output line.

Here, in FIGS. 9A and 9B, constant voltages V1 and V2 are the fixed voltages applied to the photodiode, the reset TFT, and the buffer TFT. Typically, a power source voltage or an earth voltage is employed as the fixed voltage.

One or a plurality of optical sensors with the circuit configuration shown in FIG. 9A or FIG. 9B may be provided in the sensor section of the light emitting module in accordance with the present invention. Further, the circuit configurations shown in FIGS. 9A and 9B are simply examples. While the TFTs are used as active elements here, a MOS transistor is used naturally in the case where the pixel section is formed of the MOS transistor. Still further, in the case where the TFT is used, a top gate type TFT or a bottom gate type TFT may be used.

In this respect, the configuration of the present embodiment can be carried out with any of the configurations of the embodiment 1 to the embodiment 5.

(Embodiment 7)

A circuit which functions as an optical sensor can be used as a sensor section included in a light emitting module of the present invention. In the present embodiment, an example of the circuit configuration of an passive type optical sensor will be shown in FIG. 10.

The optical sensor shown in FIG. 10 includes a photodiode 1001 and a reset TFT 1002. Also, a reset signal line 1003 is connected to the gate of the reset TFT

1002. Further, a reference numeral 1004 designates an output line.

Here, in FIG. 10, a constant voltage V1 is the fixed voltage applied to the photodiode. Typically, a power source voltage or an earth voltage is employed as the fixed voltage.

One or a plurality of optical sensors with the circuit configuration shown in FIG. 10 may be provided in the sensor section of the light emitting module in accordance with the present invention. Further, the circuit configurations shown in FIGS. 10 is simply an example. While the TFTs are used as active elements here, a MOS transistor is used naturally in the case where the pixel section is formed of the MOS transistor. Still further, in the case where the TFT is used, a top gate type TFT or a bottom gate type TFT may be used.

In this respect, the configuration of the present embodiment can be carried out with any of the configurations of the embodiment 1 to the embodiment 5.

(Embodiment 8)

In the present embodiment, the case will be described, where a structure of a pixel in a pixel section is different in from the FIG. 1B. Here, the description of FIG. 1B may be referred to on the parts with the same reference symbols as in FIG. 1B.

The configuration shown in FIG. 11A is characterized in that an erasing TFT 1101 is disposed between a switching TFT 113 and the gate of a current control TFT 114. The erasing TFT 1101 is a TFT for forcibly transforming the gate voltage applied to the current control TFT 114 to 0 volt. One of the source and drain of the erasing TFT 1101 is connected to the gate of a current control TFT 114 and the other is connected to the current supply line 118 and the gate thereof is connected to a wiring (erasing gate line) 1102 to be the gate of the erasing TFT 1101.

Further, the configuration shown in FIG. 11B is a publicly known



configuration in which a first TFT 1103, a second TFT 1104, a third TFT 1105, a fourth TFT 1106, a first capacitor 1107 and a second capacitor 1108 are provided. Still further, there is provided a data line 1109, a first gate line 1110, a second gate line 1111, a third gate line 1112, and a current supply line 1113 as shown in FIG. 11B in order to transmit signals to the respective TFTs.

A plurality of pixels with the circuit configuration shown in FIG. 11A or 11B may be formed in the pixel section of the light emitting module in accordance with the present embodiment. Further, the circuit configurations shown in FIG. 11A and 11B are simply examples. While the TFTs are used as active elements here, the pixel section may be formed of MOS transistors. Still further, a top gate type TFT or a bottom gate type TFT may be used.

In this respect, the configuration of the present embodiment can be carried out with any of the configurations of the embodiment 1 to the embodiment 7.

(Embodiment 9)

In a light emitting module in accordance with present invention, it can make an operating frequency reduced to drive a data signal side driving circuit by a publicly known split driving method. The split driving method is a driving method for reducing the operating frequency by writing a data signal in a plurality of pixels at the same time in performing a driving method by a dot sequential type driving method.

In this case,  $n$  data signals (video signals) are necessary for split-driving by  $n$  splits. A data signal is written in a block of  $n$  pixels in synchronization with the output timing of a shift register. In this way, the operating frequency of the data signal side driving circuit can be reduced to  $1/n$ .

Further, it is possible to write a data signal every  $n$  pixels in synchronization with the output timing of a shift register. Also in this case, the operating frequency of

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(Embodiment 10)

FIG. 12A is a plan view to show a state in which the process has been advanced to the step of sealing the EL element and FIG. 12B is a cross-sectional view taken on line A – A' in FIG. 12A. A section 1200 indicated by a dotted line is a pixel section and a section 1201 is a source signal side driving circuit and a section 1202 is a gate signal side driving circuit and a section 1203 is a sensor section. Also, a reference numeral 1204 designates a covering member and 1205 designates a first sealing member and 1206 designates a second sealing member.

Further, a reference numeral 1207 designates a TAB tape to be an external input terminal that receives a video signal or a clock signal from an external driving circuit and a correction circuit. Here, while only the TAB is shown, the TAB tape may be provided with a printed wiring board (PWB) or be a TCP.

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Further, the source signal side driving circuit 1201 includes a CMOS circuit made of a combination of an n-channel type TFT 453 and a p-channel type TFT 454. Still further, the sensor section 1203 includes a photodiode 452 connected to a reset TFT 451. Here, a polarizing plate (typically, a circular polarizing plate) may be placed to the insulating body 400.

The pixel electrode 423 functions as the anode of the EL element. Also, banks 424 are formed on both ends of the pixel electrode 423, and an EL layer 425 and the cathode 426 of the EL element are formed on the pixel electrode 423. The cathode 426 functions also as a wiring common to all the pixels and finally is electrically connected to a TAB tape 1207. Further, all the elements included in the pixel section 1200, the source signal side driving circuit 1201, and the sensor section 1203 are covered with a protective film 427.

Further, the covering member 1204 is placed to the insulating body 400 with the first sealing member 1205. Here, a spacer may be provided so as to ensure a gap between the covering member 1204 and the EL element. A space 1208 is formed inside the first sealing member 1205. Here, it is preferable that the first sealing member 1205 is made of a material which does not allow moisture or oxygen to pass through. Further, it is effective that a substance having effects of absorbing moisture or preventing oxidation is disposed in the space 1208.

In this respect, it is recommended that carbon films (specifically, a diamond like carbon film) 1209a, 1209b having a thickness of from 2 nm to 30 nm be formed as protective films on the obverse surface and the reverse surface of the covering member 1204. Such a carbon film prevents the entry of oxygen and water and mechanically protects the surface of the covering member 1204.

Further, after the covering member 1204 is bonded, a second sealing member

1206 is formed such that it covers the exposed surface of the first sealing member 1205. The second sealing member 1206 can be formed of the same material as the first sealing member 1205.

By sealing the EL element by the structure described above, the EL element can be completely shielded from the outside to prevent a substance for degrading the EL layer by oxidation such as moisture and oxygen from entering from the outside. Accordingly, this can provide a light emitting device having high reliability.

As shown in FIGS. 12A and 12B, a light emitting module with a pixel section, a driving circuit, and a sensor section which are formed on the same insulating body, and further provided with a TAB tape, is referred to as a light emitting module of a built-in driving circuit type in the present specification.

(Embodiment 11)

In the embodiment 10, the light emitting module of a built-in driving circuit type shown in FIGS. 12A and 12B is an example in which a pixel section and a driving circuit are integrally formed on the same insulating body, but the driving circuit may be provided as an external IC (integrated circuit). In this case, the structure will be shown in FIG. 13A.

In the module shown in FIG. 13A, a TAB tape 14 is attached to a substrate (active matrix substrate) 10 (including a pixel section 11, wirings 12a, 12b and a sensor section 13) on which a pixel section including a TFT and an EL element is formed and a printed wiring board 15 is connected thereto through the TAB tape 14. Here, the circuit block diagram of the printed wiring board 15 is shown in FIG. 13B.

As shown in FIG. 13B, at least I/O ports (also referred to as an input/output section) 16, 19, a source signal side driving circuit 17, a gate signal side driving circuit 18 and an IC which functions as a correction circuit 20 are mounted in the printed

wiring board 15.

As described above, a module with a configuration, in which a substrate with a pixel section and a sensor section which are formed on the same insulating body is provided with a TAB tape and a printed wiring board which functions as a driving circuit is provided via the TAB tape, is referred to as a light emitting module of an external driving circuit type in the present specification.

Further, in the module shown in FIG. 14A, a light emitting module of a built-in driving circuit type 30 (including a pixel section 31, a source signal side driving circuit 32, a gate signal side driving circuit 33, wirings 32a, 33a, a sensor section 34, and a TAB tape 35) is provided with a printed wiring board 36 via the TAB tape 35. Here, the circuit block diagram of the printed wiring board 36 is shown in FIG. 14B.

As shown in FIG. 14B, at least I/O ports 37, 40, a control section 38 and an IC which functions as a memory section 39 are provided in the printed wiring board 36. Here, the memory section 39 functions as the correction memory shown in FIG. 2 or FIG. 3 and luminance is adjusted by the correction circuit included in the control section 38. Also, the control section 38 can control various kinds of signals such as a signal transmitted to the driving circuit or a timing signal.

As described above, a module with the configuration, in which a light emitting module of a built-in driving circuit type with a pixel section, a driving circuit and a sensor section which are formed on the same insulating body is provided with a printed wiring board which functions as a controller, is referred to as a light emitting module of an external controller type.

(Embodiment 12)

A light emitting module formed by the use of the present invention is applied

to various kinds of electric appliances and a pixel section is used as an image display section. The electric appliances in accordance with the present invention include a video camera, a digital camera, a goggle type display (head-mounted display), a navigation system, an audio unit, a note-type personal computer, a game device, a portable device (a mobile computer, a portable telephone, a portable game device or an electronic book), and an image reproducing device provided with a recording medium. The specific examples of these electric appliances will be shown in FIGS. 15A to 16B.

FIG. 15A is an EL display and includes a box 2001, a support 2002, and a display section 2003. A light emitting module in accordance with the present invention can be used as the display section 2003. By applying the light emitting module in accordance with the present invention to the display section, it is possible to improve the visibility of the EL display and to reduce power consumption.

FIG. 15B is a video camera and includes a main body 2101, a display section 2102, a voice input section 2103, an operating switch 2104, a battery 2105, and an image receiving section 2106. The light emitting module in accordance with the present invention can be used as the display section 2102.

FIG. 15C is a digital camera and includes a main body 2201, a display section 2202, an eyepiece section 2203, and an operating switch 2204. The light emitting module in accordance with the present invention can be used as the display section 2202.

FIG. 15D is an image reproducing device provided with a recording medium (specifically, a DVD reproducing device) and includes a main body 2301, a recording medium (a CD, an LD, or a DVD) 2302, an operating switch 2303, a display section (a) 2304 and a display section (b) 2305. The display section (a) 2304 mainly displays image information and the display section (b) 2305 mainly displays textual

information and a light emitting module in accordance with the present invention can be used as the display sections (a), (b). Here, the image reproducing device provided with the recording medium includes a CD reproduction device, a game device and the like.

FIG. 15E is a portable (mobile) computer and includes a main body 2401, a display section 2402, a image receiving section 2403, an operating switch 2404, and a memory slot 2405. The light emitting module in accordance with the present invention can be used as the display section 2402. This portable computer can record information on a recording medium in which flash memories and non-volatile memories are integrated and can reproduce the information.

FIG. 15F is a personal computer and includes a main body 2501, a box 2502, a display section 2503, and a keyboard 2504. The light emitting module in accordance with the present invention can be used as the display section 2503.

Further, occasions are increasing when the above-mentioned electric appliances display information allotted through an electronic communication line such as an internet, a CATV (cable TV), and the like and, in particular, occasions are increasing when the appliances display motion pictures. In the case where a light emitting module using an EL element is used as a display section, it is possible to display motion pictures without delay because the EL element has a vary high response speed.

Still further, since the light emitting section consumes electric power in the light emitting module, it is preferable that information be displayed such that the light emitting section is made as small as possible. Accordingly, in the case a light emitting module is used as a display section mainly displaying textual information such as a portable information terminal, in particular, a portable telephone, an audio

unit, or the like, it is preferable that the light emitting module be driven in such a way that textual information is formed of the light emitting section against the background of non-light emitting section.

Here, FIG. 16A shows a portable telephone and has a key operating section (operation section) 2601 and an information displaying section (information display section) 2602 which is connected to the operation section 2601 with a connection section 2603. Also, the operation section 2601 is provided with a voice input section 2604 and an operating key 2605 and the information display section 2602 is provided with a voice output section 2606 and a display section 2607.

The light emitting module in accordance with the present invention can be used as the display section 2607. In this respect, in the case where the light emitting module is used as the display section 2607, displaying white letters against a black background can reduce power consumption of the portable telephone.

In the case of the portable telephone shown in FIG. 16A, it is also possible to use the portable telephone as a certification system terminal for certifying a user by reading his fingerprint or palm by building a sensor made of a CMOS circuit (CMOS sensor) in the light emitting module used as the display section 2604. Further, it is possible to read the brightness (illuminance) of the outside and to emit light such that information is displayed in a set contrast.

Still further, it is possible to reduce the power consumption of the portable telephone by decreasing the luminance of the display section 2604 while the operating switch 2605 is being used and increasing the luminance after the use of the operating switch is finished. Still further, it is possible to reduce the power consumption of the portable telephone by increasing the luminance of the display section 2604 when a signal is received and decreasing the luminance during a telephone conversation.



Still further, it is possible to reduce the power consumption of the portable telephone by providing it with a function of turning off the display by a time control unless reset while it is continuously used. Here, these functions may be manually controlled.

Still further, FIG. 16B shows an audio unit mounted in a motor vehicle and includes a box 2701, a display section 2702, and operating switches 2703, 2704. The light emitting module in accordance with the present invention can be used as the display section 2702. While an audio unit (car audio unit) mounted in a motor vehicle is shown as an example of the audio unit in the present embodiment, the light emitting module in accordance with the present invention can be applied to an audio unit mounted in the house (audio component). Here, in the case where the light emitting module is used as the display section 2704, it is possible to reduce power consumption by displaying white letters against a black background.

As described above, the present invention has a wide range of application and can be applied to various kinds of electric appliances. As a result, it is possible to improve the visibility of the display section of the electric appliance and to reduce the power consumption by using a light emitting module which is excellent in visibility in the bright environment and the dark environment and further can reduce power consumption to a minimum in the dark environment. Further, the light emitting module including any of the configurations of from the embodiment 1 to the embodiment 11 may be applied to the electric appliances of the present embodiment.

(Effects of the Invention)

According to the present invention, environmental illuminance is sensed by a sensor section provided in a light emitting device, and the correct luminance of an EL element and a correction signal necessary for obtaining the correct luminance are calculated by a correction circuit based on the output signal of the sensor section.

Then, the amount of current flowing through the EL element is corrected based on the correction signal to keep the ratio of the luminance of the EL element to the environmental illuminance at a constant value.

As a result, according to the present invention, it is possible to obtain a light emitting module which is excellent in visibility in the bright environment and in the dark environment and can reduce power consumption to a minimum in the dark environment. Therefore, an electric appliance employing the light emitting module in accordance with the present invention can have excellent visibility in a display section and reduce power consumption.

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